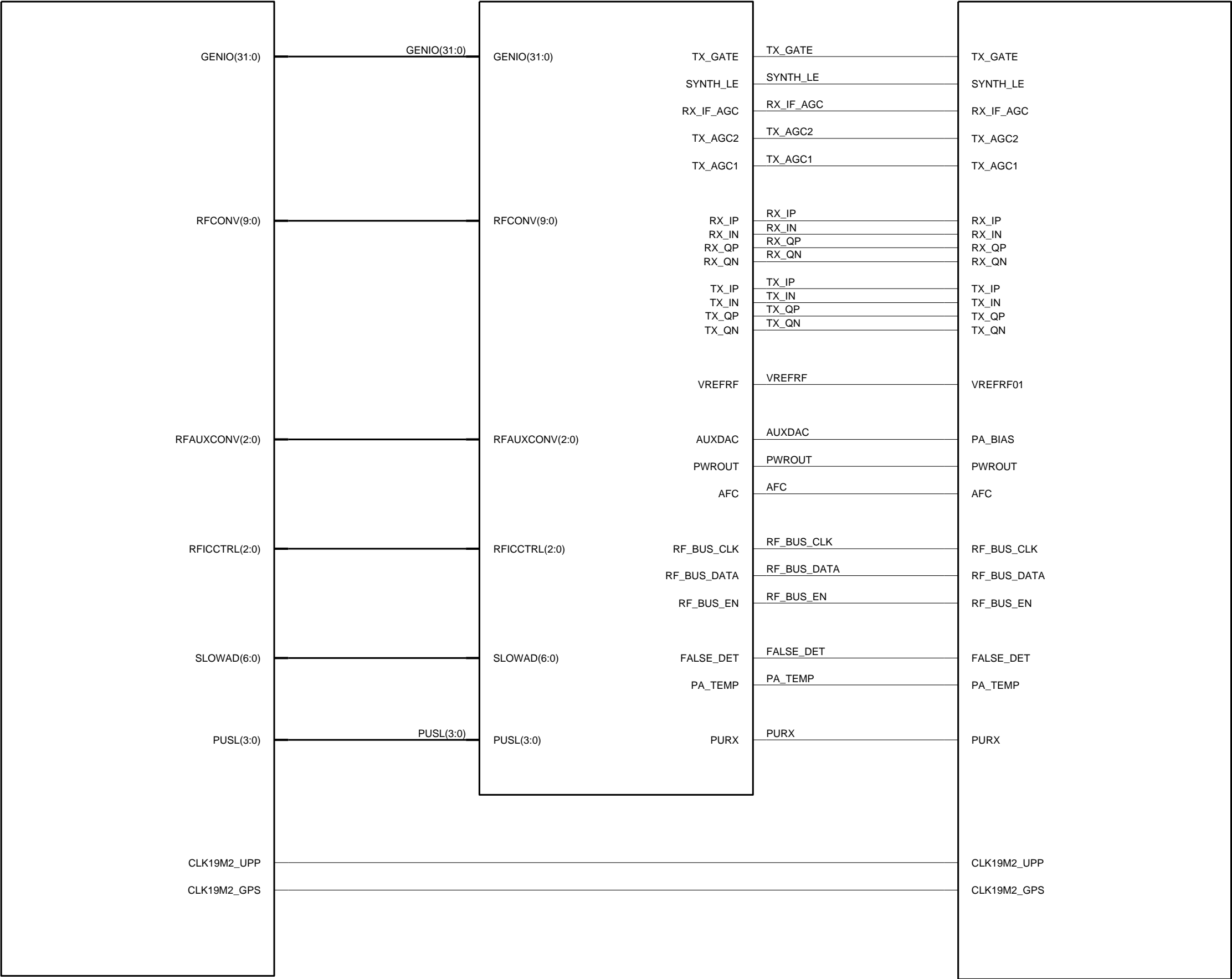


Top Level

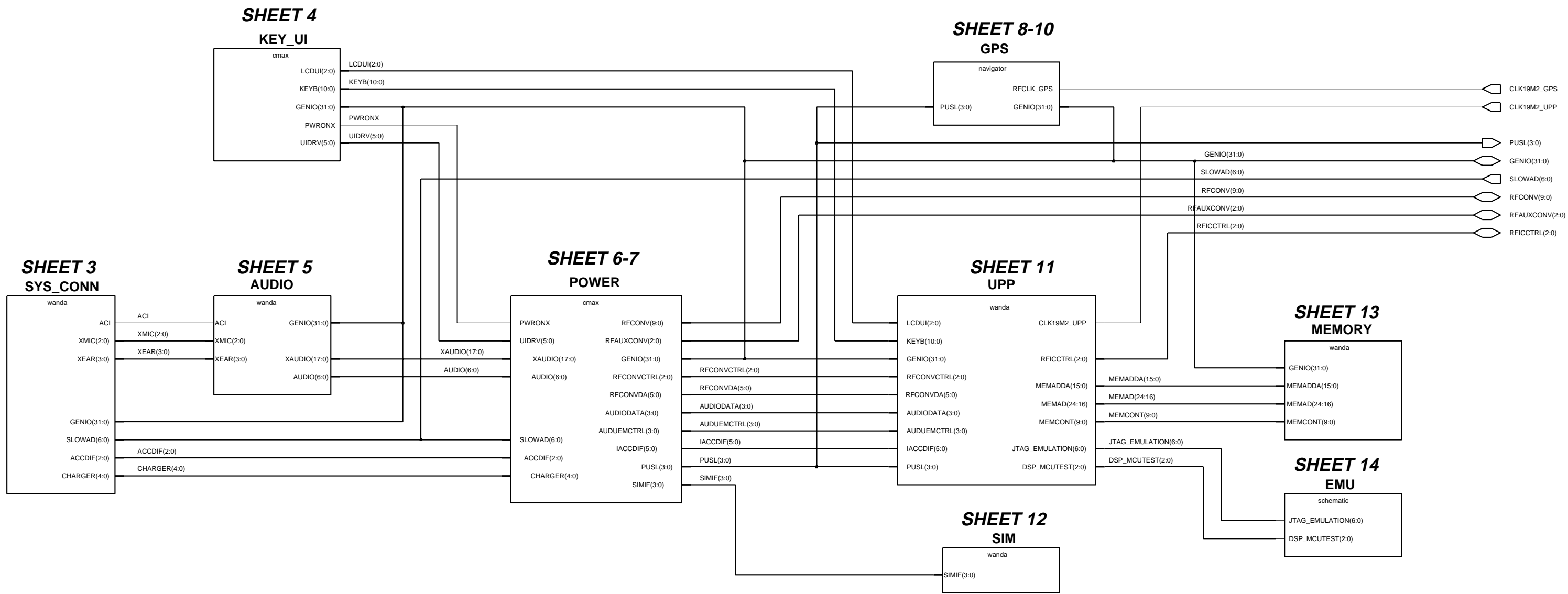
SYS

RF_BB

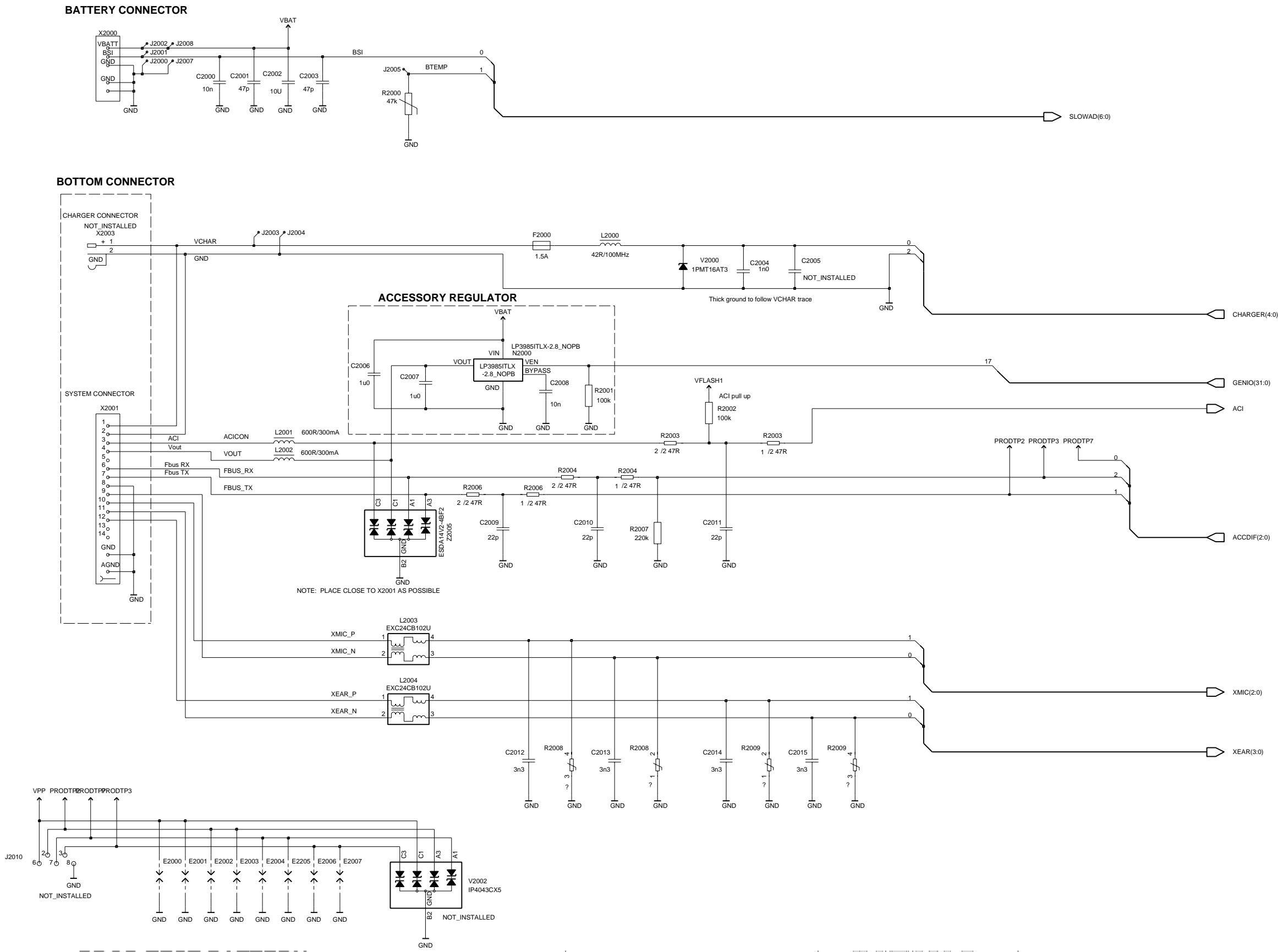
RF



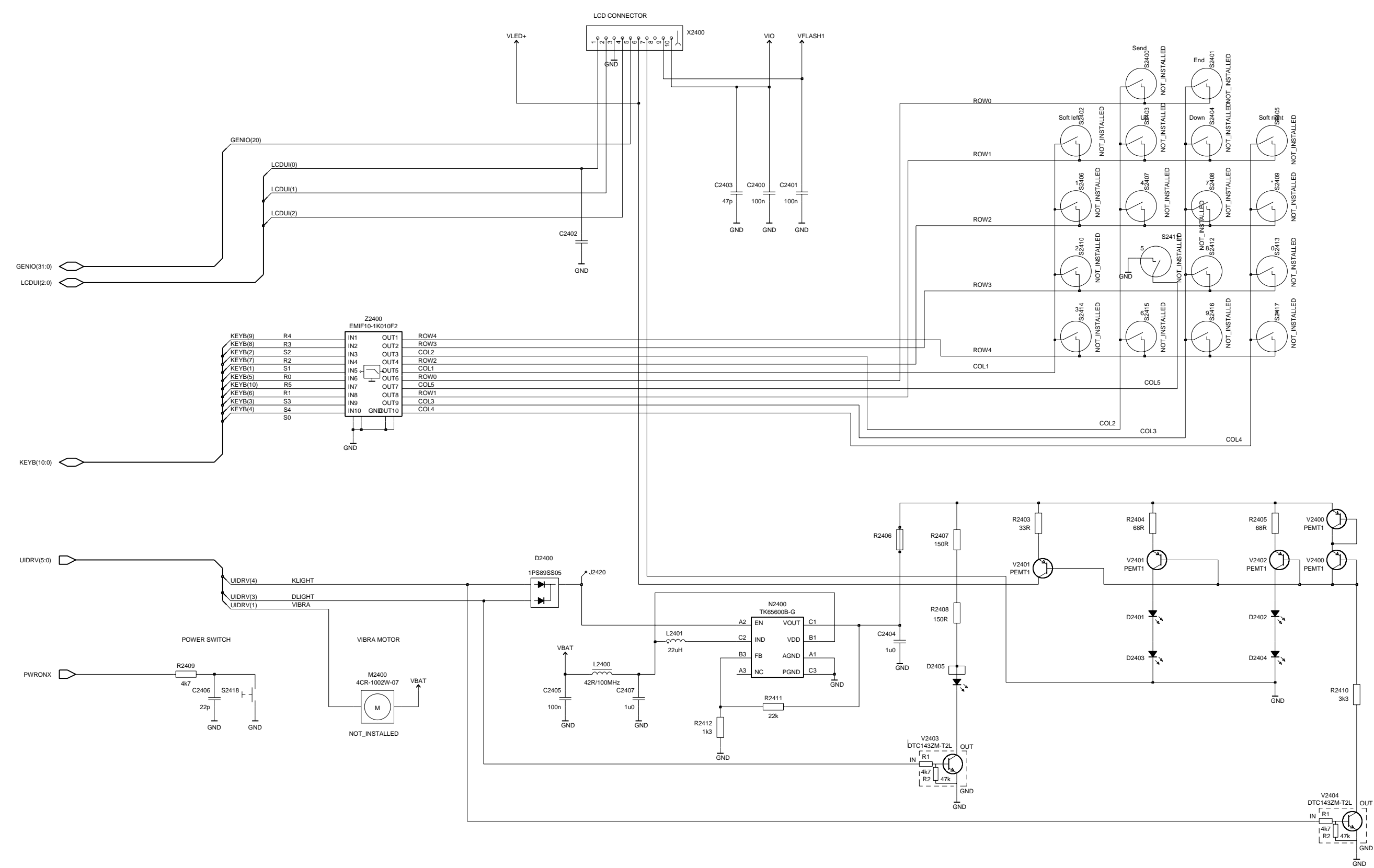
Baseband - Top Level



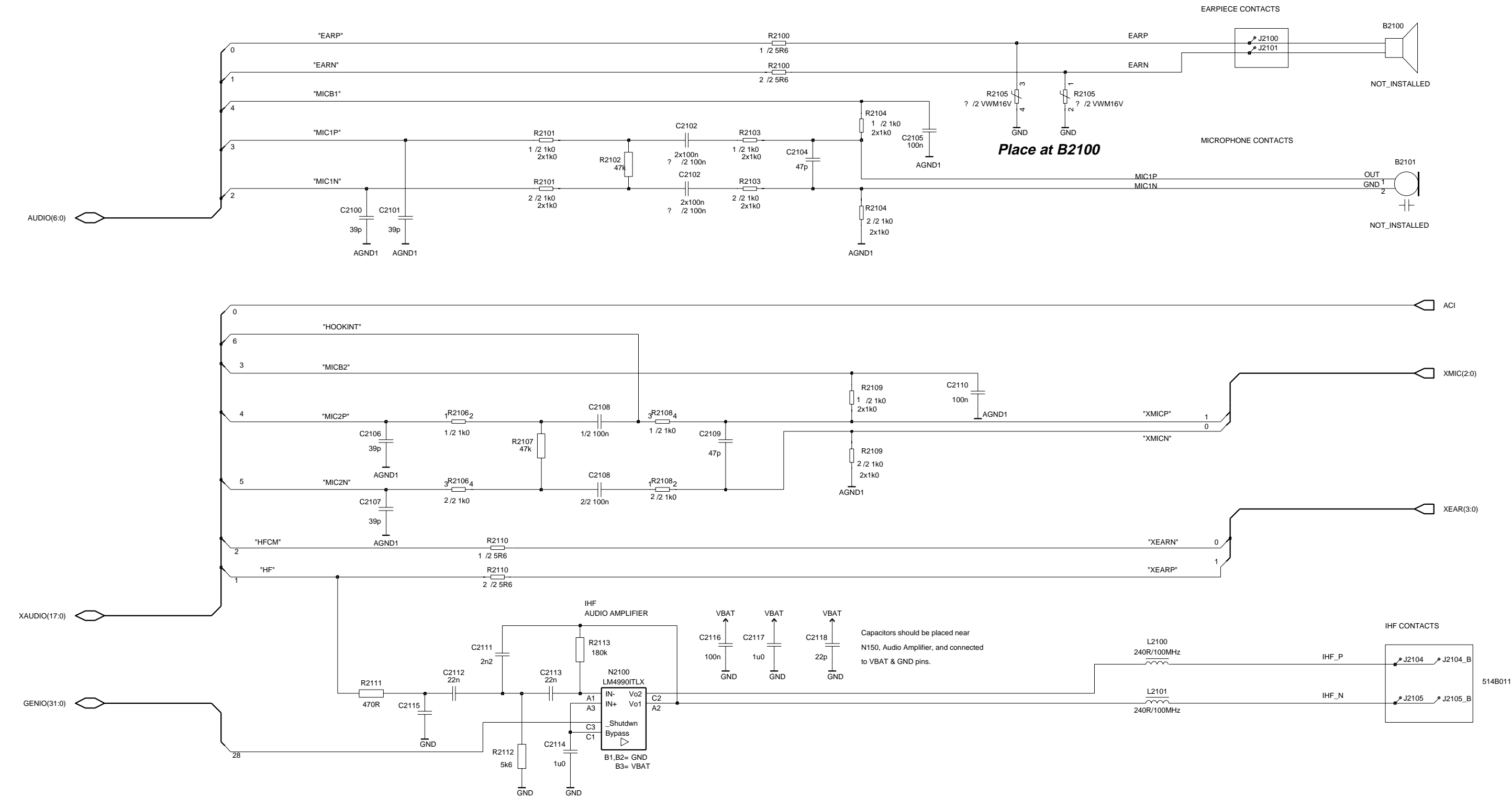
System Connector



User Interface



Audio



The diagram illustrates the PIPPI system architecture. It features a central component, the R2700 EMIF03-SIM01F2, which acts as a bridge between the SIMIF(3:0) input and the X2700 91485-0001 SIM reader. The SIMIF(3:0) input is connected to the R2700's CLK, SIMRST, SIMCLKO, and SIMIO DAO pins. The R2700's CLK, SIMRST, and SIMCLKO pins are connected to the X2700's SIMCLK, SIMRST, and VSIM pins, respectively. The X2700's SIMDATA pin is connected to the SIMIF(3:0) output. The X2700's VSIM pin is connected to a 100nF capacitor and ground. The X2700's GND pin is connected to ground. The R2700's GND pin is connected to ground. The R2700's VSIM pin is connected to the VSIM input.

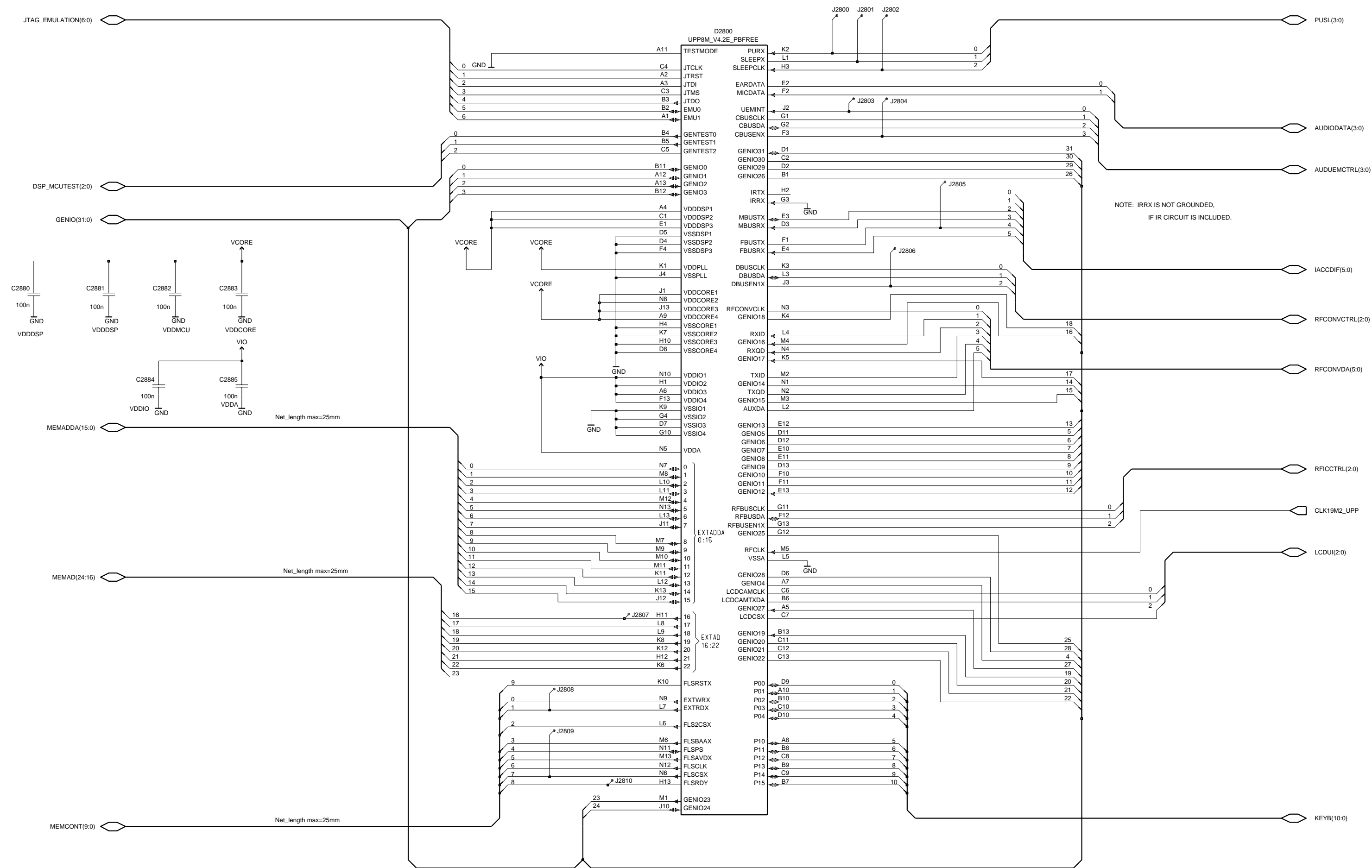
The schematic diagram illustrates the LM2708HTLX-1.35_NOPB (N2300) converter circuit. The circuit is powered by VBAT and produces Vcore. It includes an input capacitor C2301 (100nF), an input inductor L2300 (42R/100MHz), an output capacitor C2300 (10U), and an output resistor R2300. The converter is configured with SGND, VDD, PVIN, SWSYNC/MODE, and PGND pins connected to ground, and FB, VSEL, ISEL, EN, and A1 pins connected to the output. The output is filtered by C2300 and R2300. The circuit is controlled by UEMRSTX, SMPCLK, and PUSL(3:0) signals.

SHEET 6

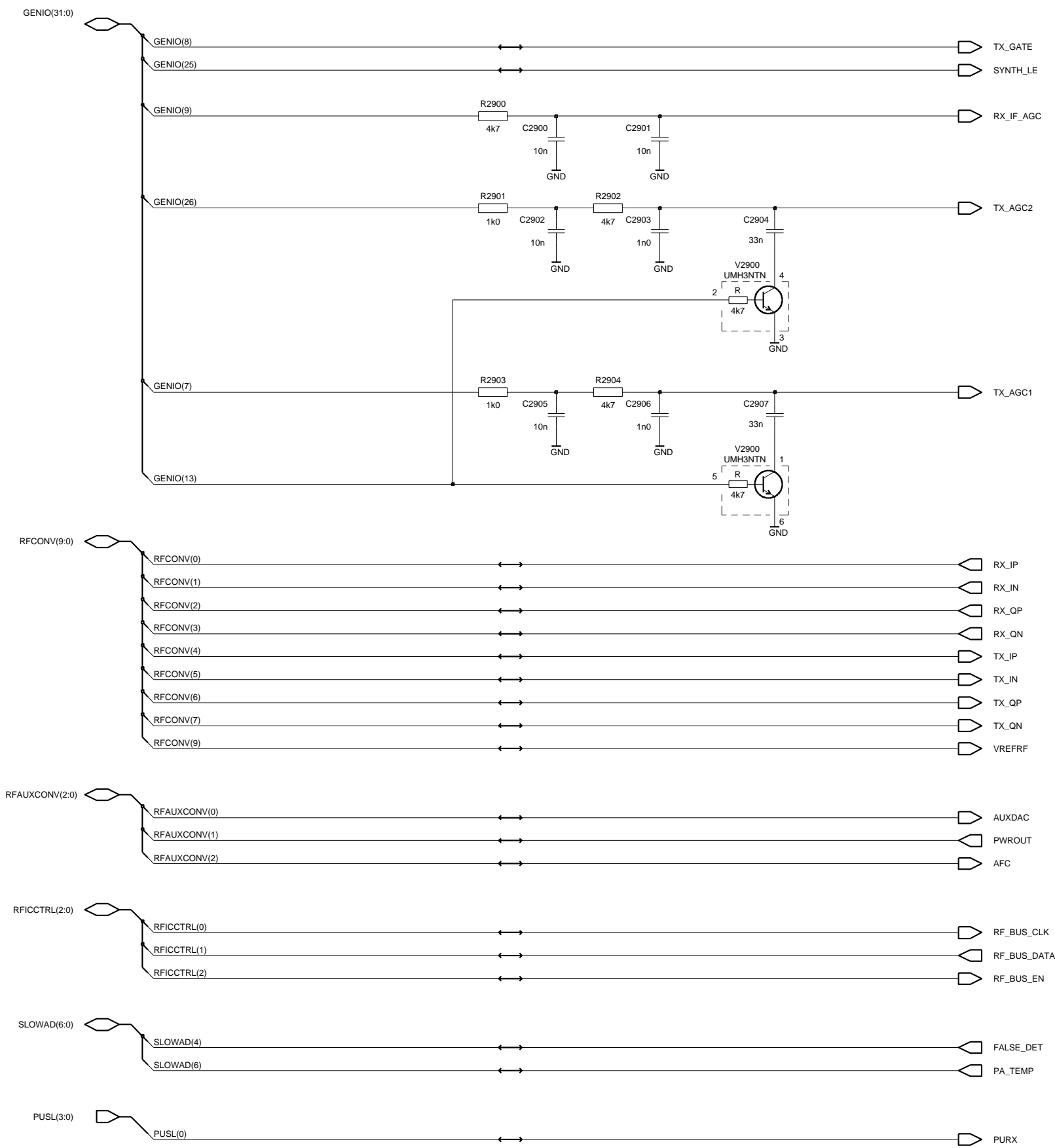


"Both 0603 and 0805 size 1uF capacitors used !"

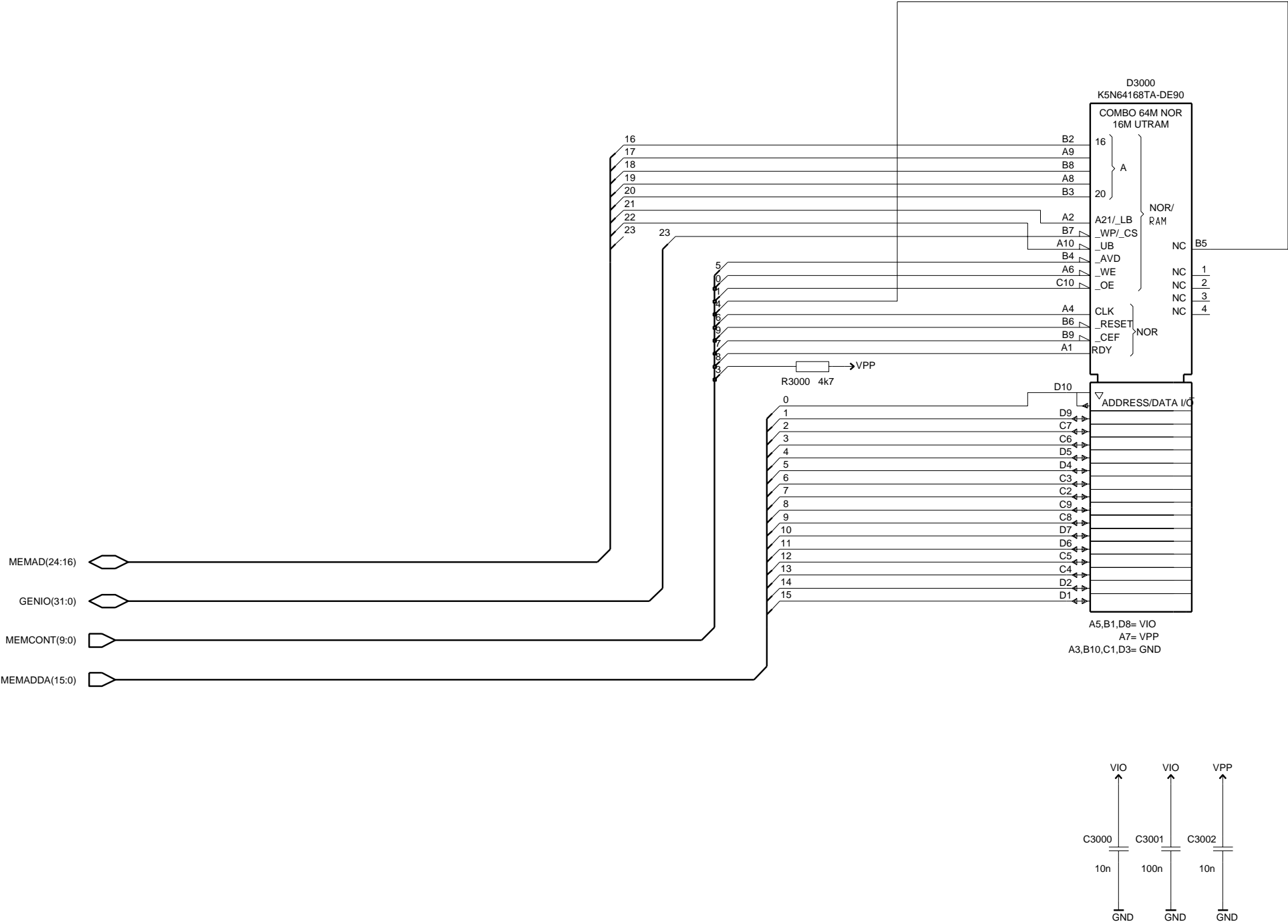
UPP



RF-BB Interface

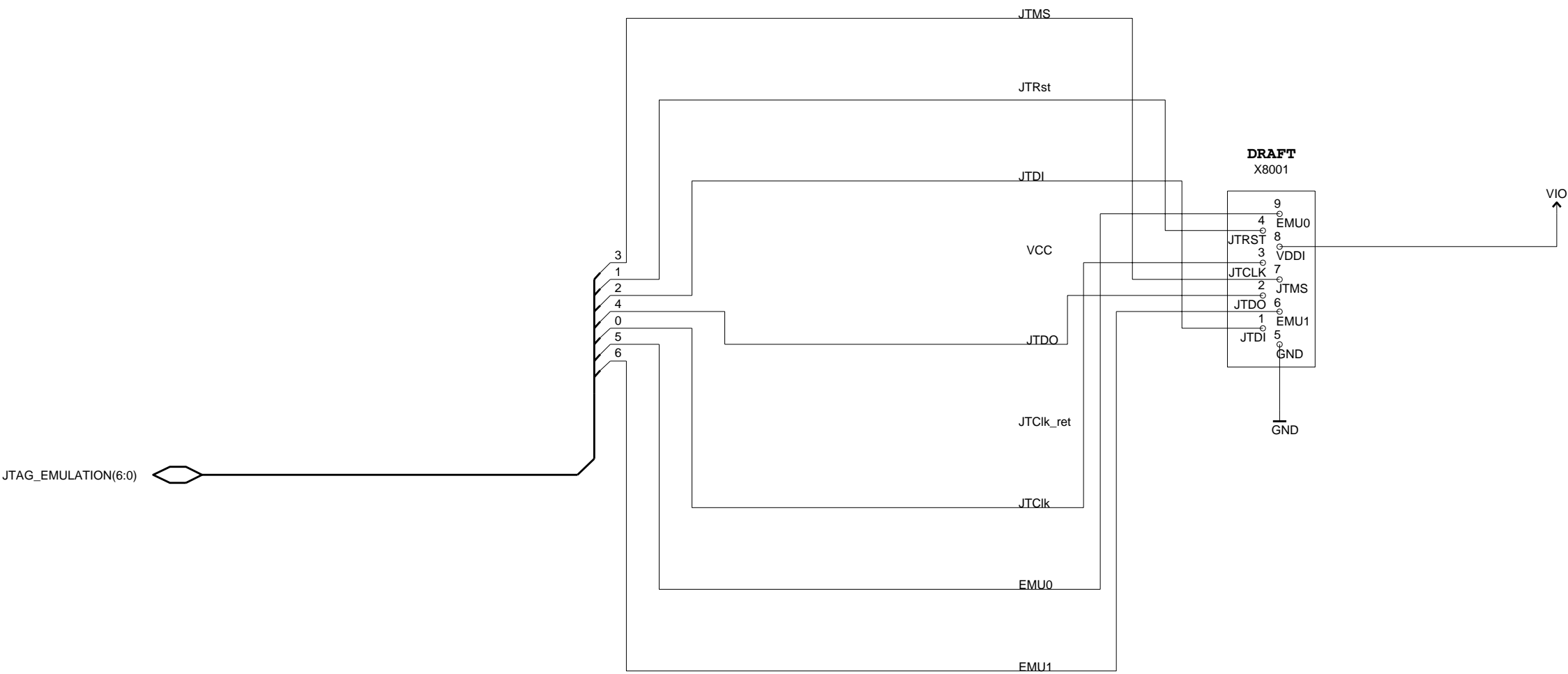


Memory



JTAG and Ostrich Connectors

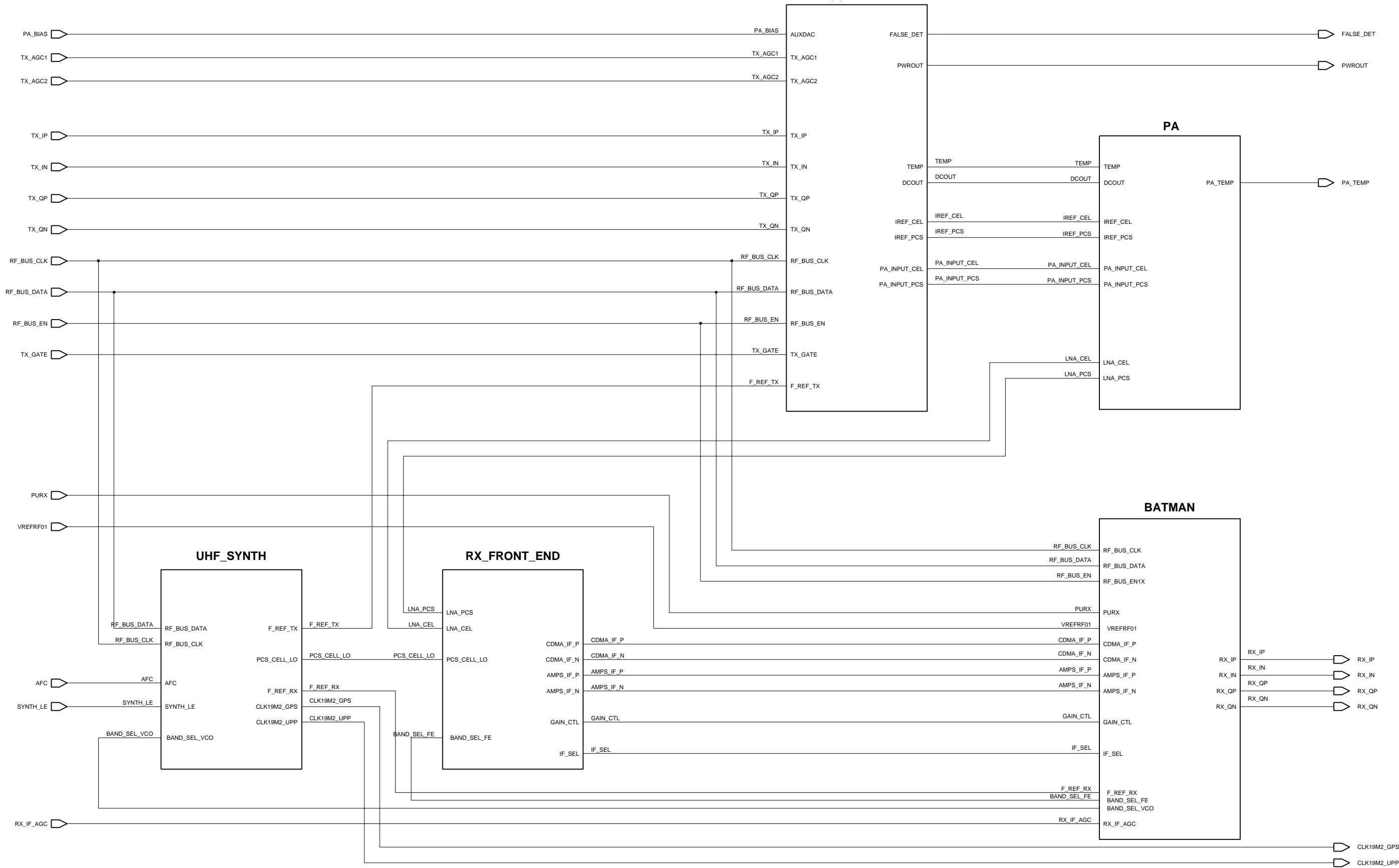
JTAG



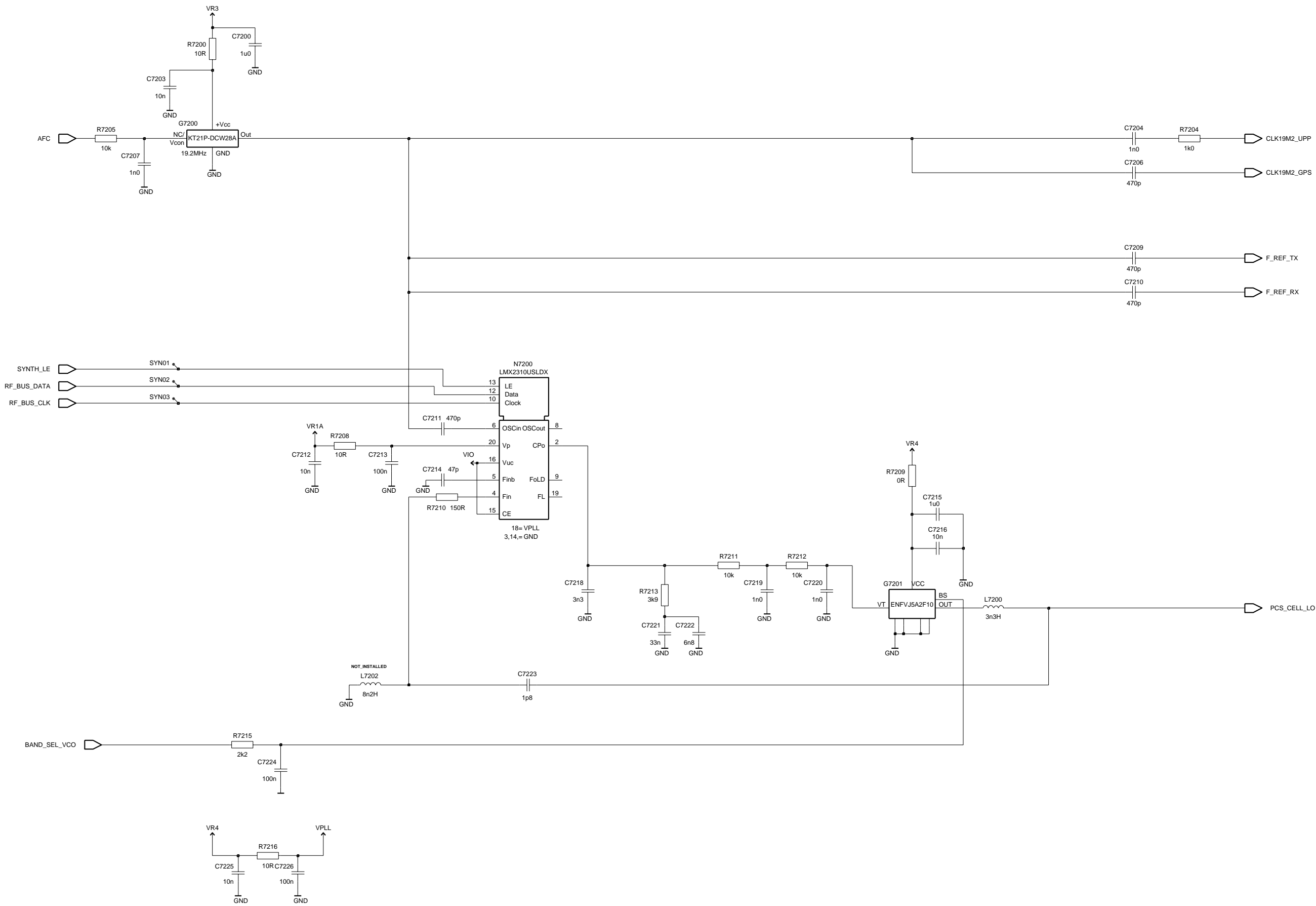
OSTRICH



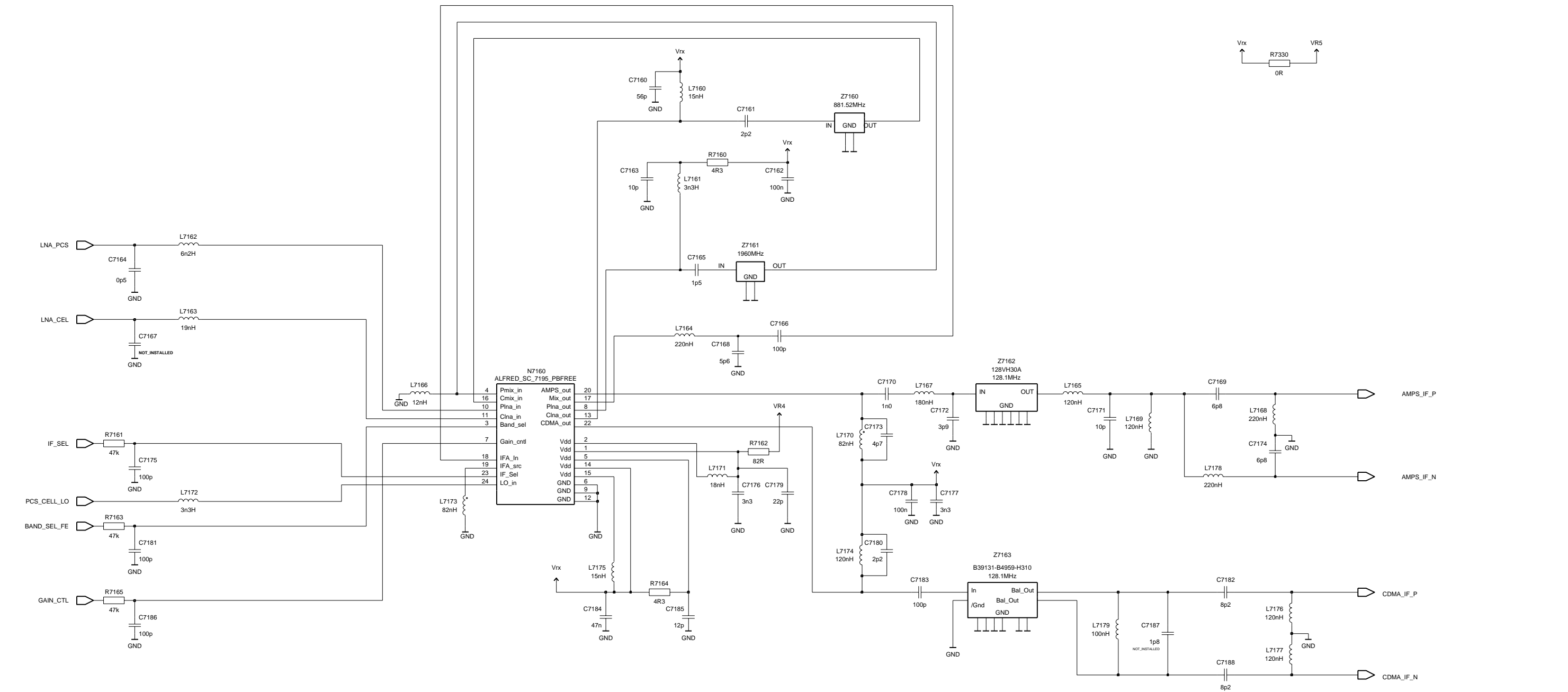
RF - Top Level



UHF Synthesizer

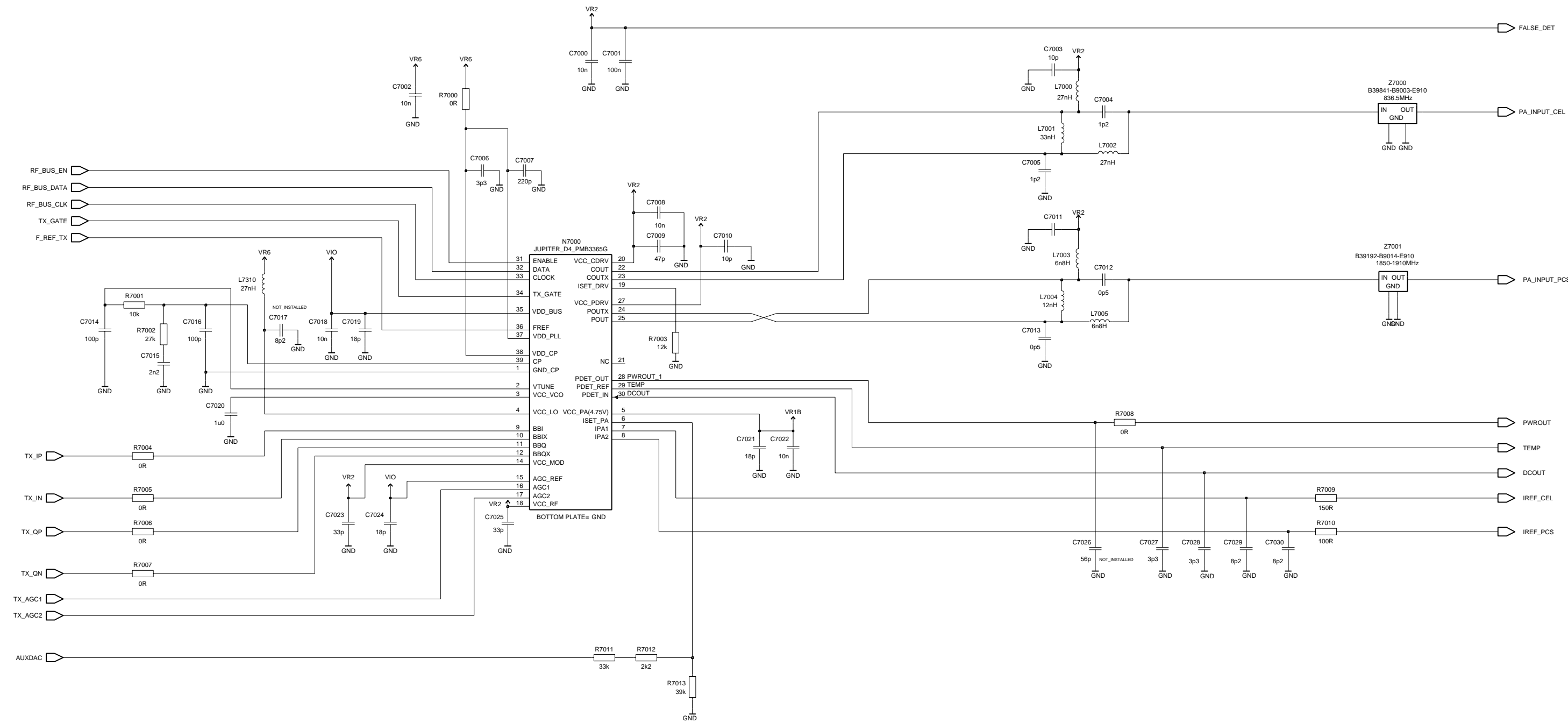


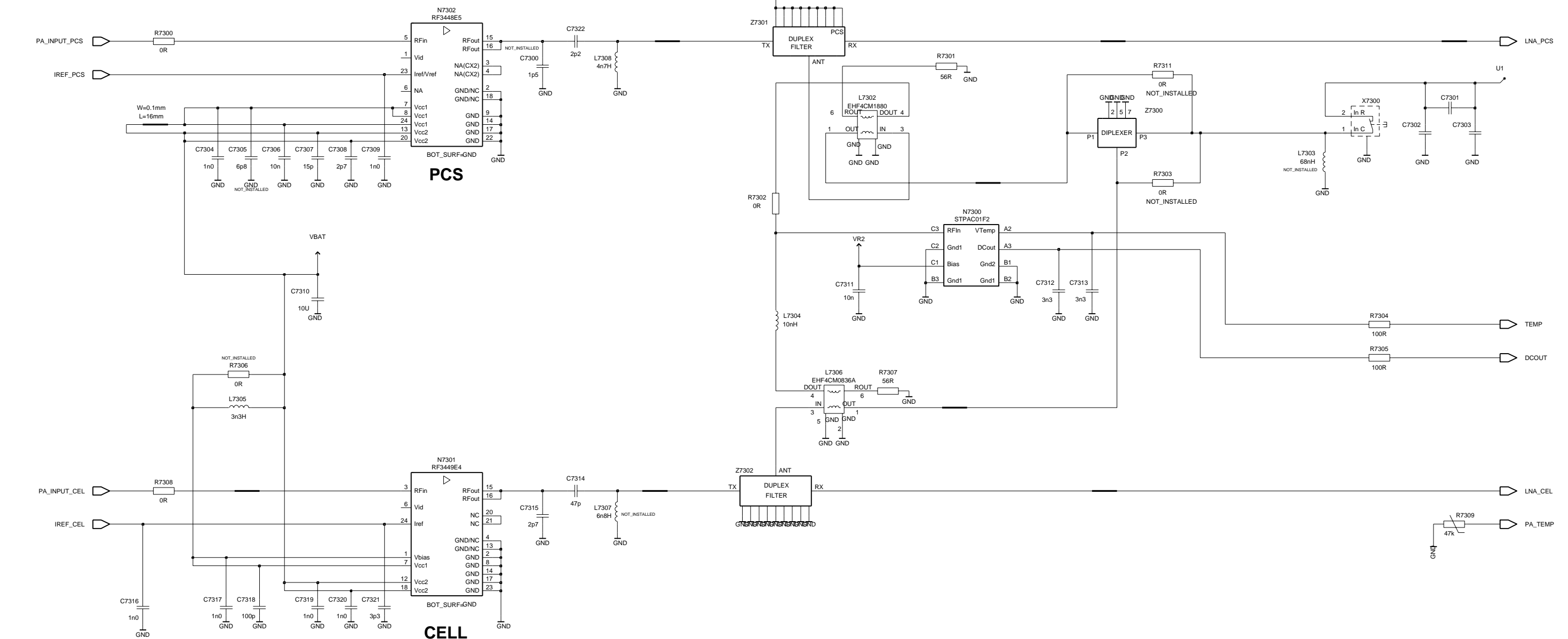
Front End (N7160)



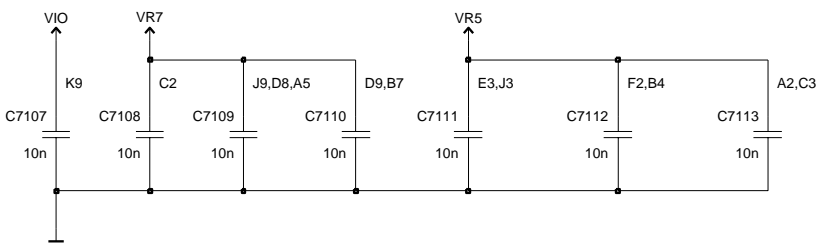
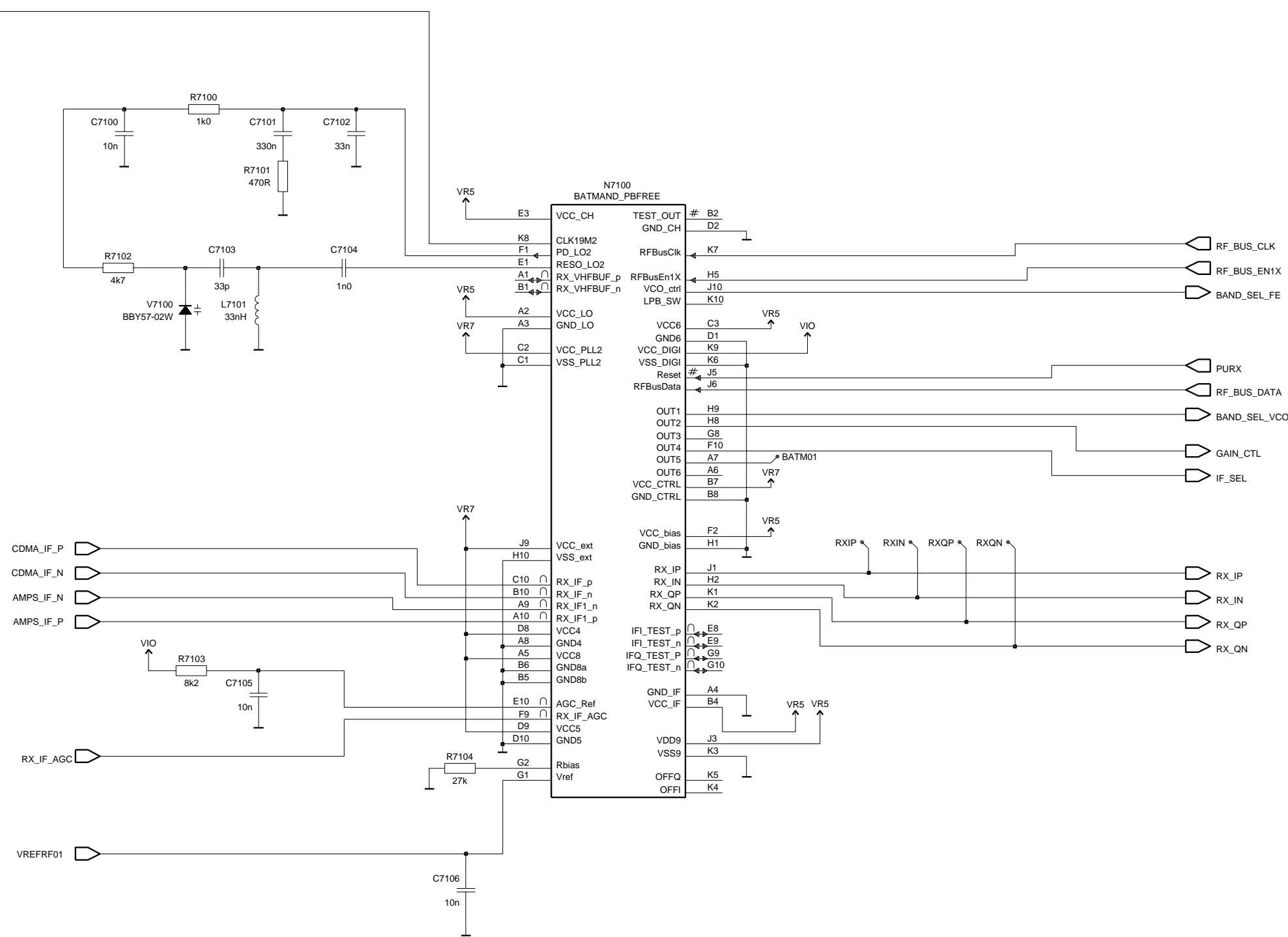
MODES	LOGIC INPUTS		
	BAND	GAIN_CTL	IF_SEL
CEL CDMA Hi Gain	0	1	0
CEL CDMA Lo Gain	0	0	0
PCS CDMA Hi Gain	1	1	0
PCS CDMA Lo Gain	1	0	0
AMPS Hi Gain	0	1	1
AMPS Lo Gain	0	0	1

N7000

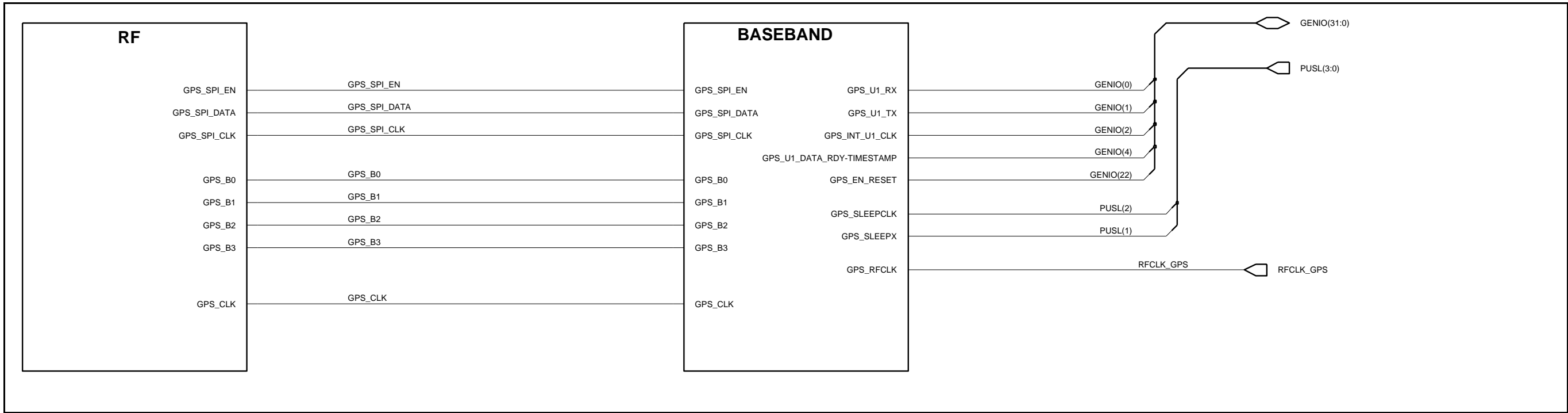




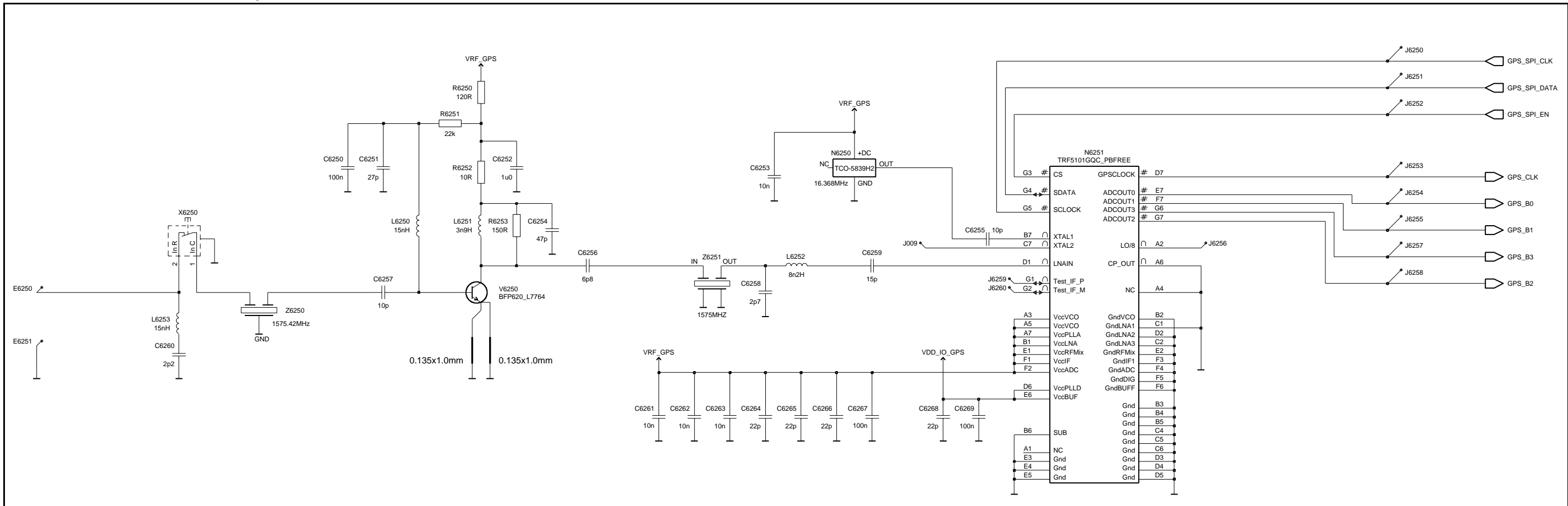
N7100



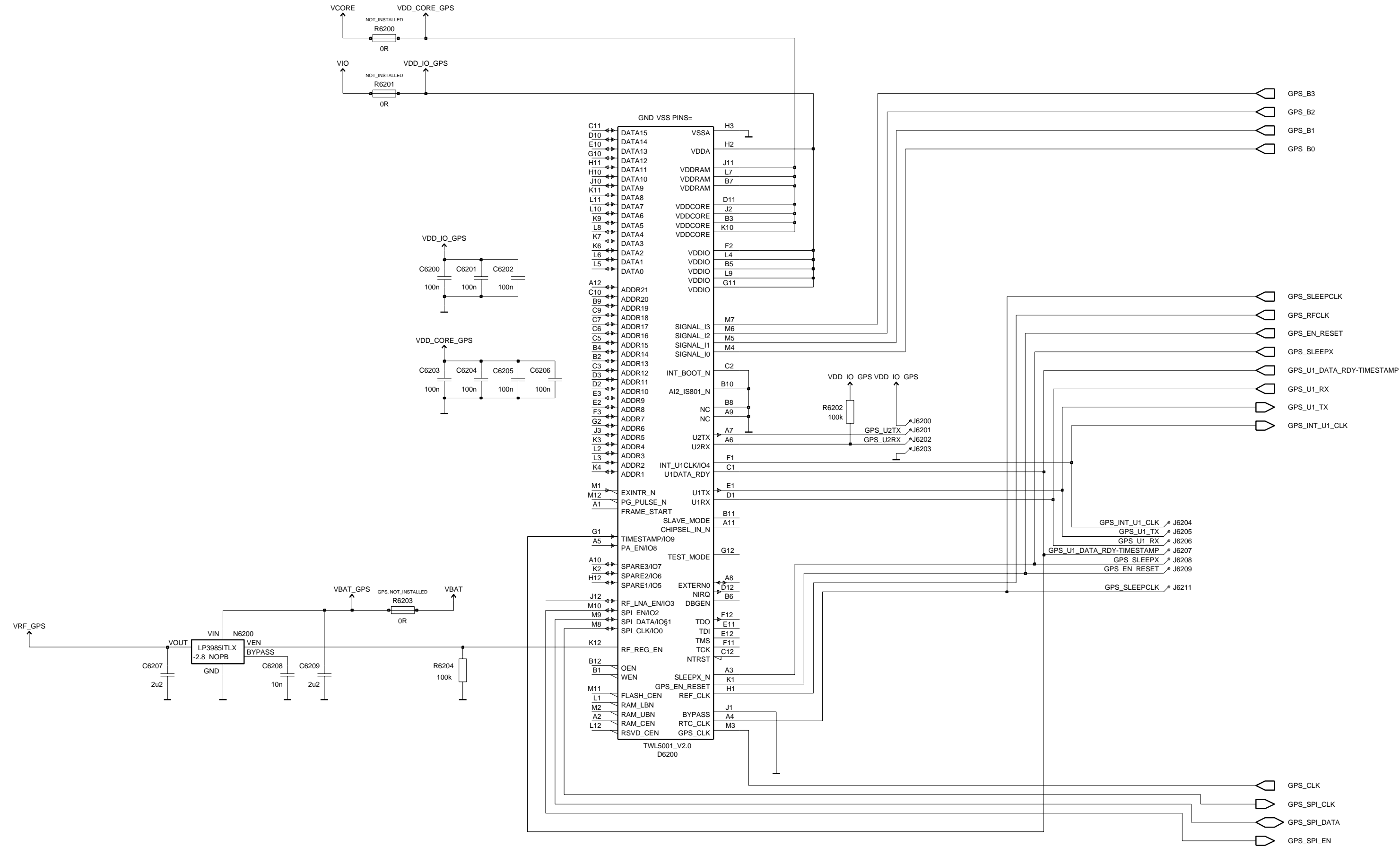
GPS RF-BB Top Level (2115i and 2116i only)



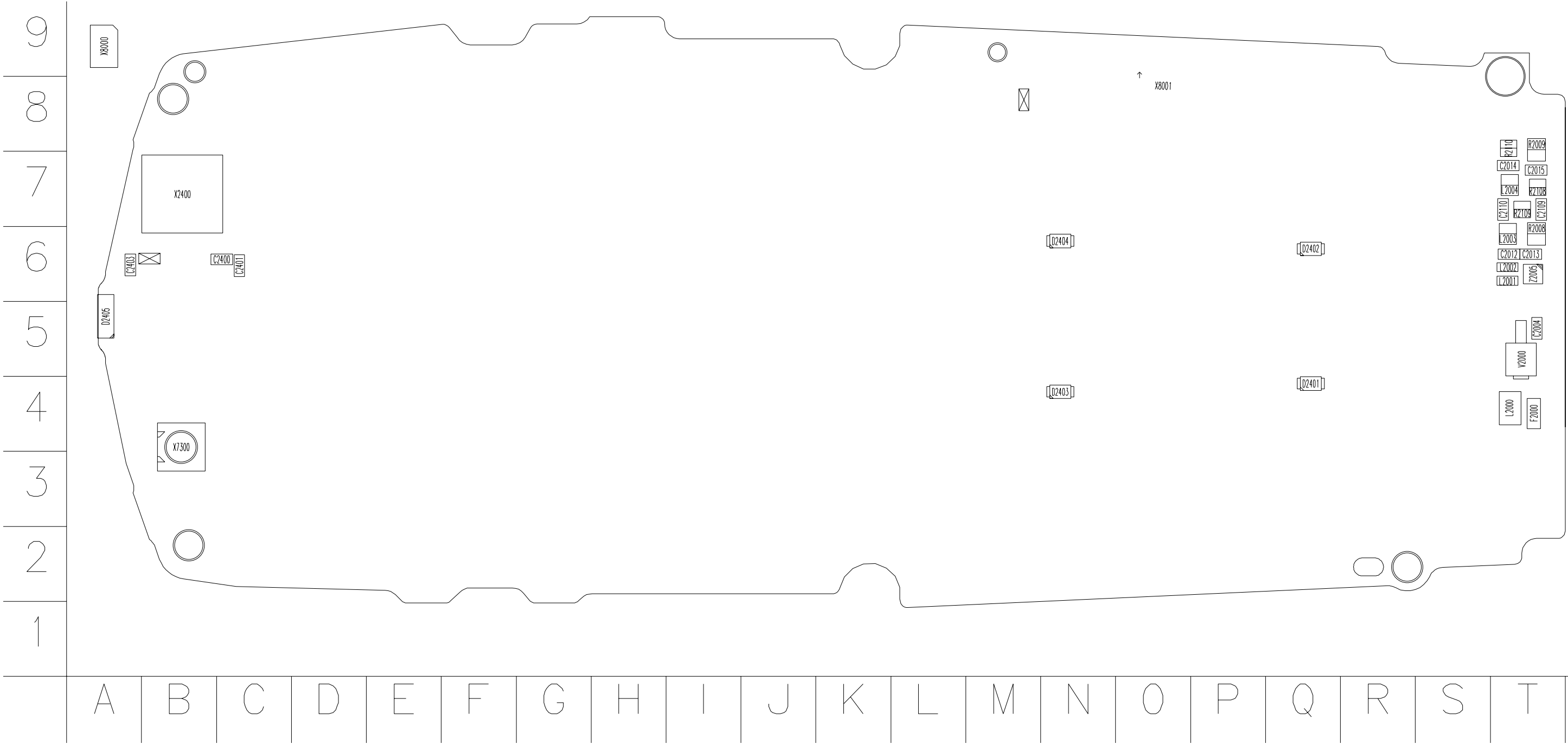
GPS RF (2115i and 2116i only)



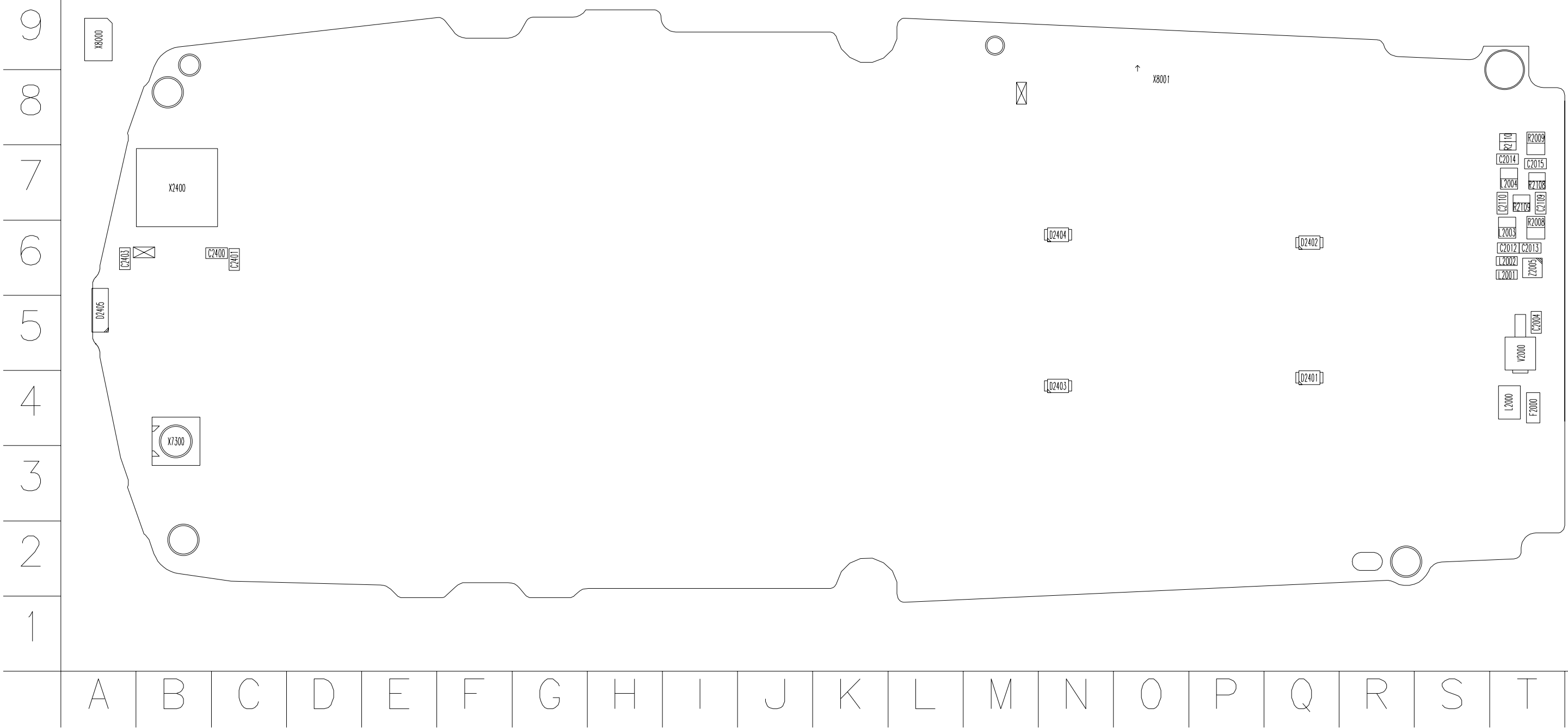
GPS Baseband (2115i and 2116i only)



2115i/2116i Component Layout - Top



2116 Component Layout - Top



This page intentionally left blank.